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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/454,941	12/02/1999	DAVID B. KIRK	1391P	4446
75	7590 10/01/2004		EXAMINER	
Wagner Murabito & Hao LLP			KIM, HAROLD J	
Two North MA: Third Floor	rket Street		ART UNIT	PAPER NUMBER
San Jose, CA	95113		2182	
			DATE MAILED: 10/01/2004	4

Please find below and/or attached an Office communication concerning this application or proceeding.



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· .	Application No.	Applicant(s)	99
	09/454,941	KIRK, DAVID B.	
Office Action Summary	Examiner	Art Unit	
	Harold Kim	2182	
The MAILING DATE of this communication ap	pears on the cover sheet w	vith the correspondence address	,
Period for Reply	VIC CET TO EVRIPE 31	MONTH(S) EROM	
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailine earned patent term adjustment. See 37 CFR 1.704(b).	.136(a). In no event, however, may a ply within the statutory minimum of th d will apply and will expire SIX (6) MC te. cause the application to become	reply be timely filed irty (30) days will be considered timely. INTHS from the mailing date of this communical	tion.
Status			Ĭ
1) Responsive to communication(s) filed on 01.	<u>June 2004</u> .		
, - ·	is action is non-final.		
3) Since this application is in condition for allows closed in accordance with the practice under			is
Disposition of Claims			
4) Claim(s) 1-28 is/are pending in the application	n.		
4a) Of the above claim(s) is/are withdra		·	
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>1-28</u> is/are rejected.			
7) Claim(s) is/are objected to.			
8) Claim(s) are subject to restriction and/	or election requirement.		5
Application Papers			
9) The specification is objected to by the Examir	ner.		ļ
10) The drawing(s) filed on is/are: a) ac		o by the Examiner.	
Applicant may not request that any objection to th			
Replacement drawing sheet(s) including the corre			
11) The oath or declaration is objected to by the E	Examiner. Note the attach	ed Office Action or form PTO-152	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of:	gn priority under 35 U.S.C	. § 119(a)-(d) or (f).	
1. Certified copies of the priority docume	nts have been received.		
2. Certified copies of the priority docume	nts have been received in	Application No	
3. Copies of the certified copies of the pr	iority documents have bee	en received in this National Stage	
application from the International Bure			
* See the attached detailed Office action for a lie	st of the certified copies n	ot received.	
Attachment(s)			
Attachment(s) 1) Notice of References Cited (PTO-892)	4) 🗍 Intervie	v Summary (PTO-413)	
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper N	o(s)/Mail Date	
Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0 Paper No(s)/Mail Date	5) Notice 6 6) Other: _	of Informal Patent Application (PTO-152)	
.S. Patent and Trademark Office		- 4 - CD No B4 9 D	00004

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DETAILED ACTION

- 1. This Office Action is in response to the filing of the Amendment, Paper # 09192004, on 6/1/04. Applicant's arguments with respect to amended claims 1-28 have been considered but they are not persuasive.
- 2. Claims 1-28 are presented for examination.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 1-3, 6, 9-13, 16-17, 20-21 and 25-26 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Dye, US Patent no. 6,173,381, in view of Davis et al., US Patent no. 4,991,169.
- 5. In re claim 1, Dye shows a controller chip [fig 5] comprising:

an engine [202, 204, 206, 210, 212, 214, 216, 221, 222, 230 fig 5] operative to manage a memory [110, fig 2], the engine including an interface [202, fig 5]; and

a storage element [230, fig 5] coupled to the engine, the storage element being accessible by a central processing unit (CPU) [120, fig 2] through the engine, wherein the engine receives commands from the CPU via the interface, and manages the storage element via the interface, and write the commands into the memory.

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Dye does not show the engine incorporates the storage element as part of the memory. Davis et al. shows the storage section is part of the shared memory [col 4, lines 1-4]. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include the storage element as part of the memory as shown in Davis et al. for saving the cost of memory by utilizing system memory as the storage elements.

- 6. In re claim 2, Dye shows FIFO buffer [230, fig 5; stores instructions for the graphic engine, col 3, lines 10-13].
- 7. In re claims 3 and 6, Dye does show FIFO buffer [204, 206, 214, 216, fig 5]. Dye does not show circular buffer and the effective size of the FIFO buffer as view by the CPU can be as large as the memory. Davis et al show the circular buffer [col 4, lines 4] and the effective size of the FIFO buffer as view by the CPU can be as large as the memory [col 4, lines 1, "shared memory"]. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include the circular buffer and the effective size of the FIFO buffer as view by the CPU can be as large as the memory as shown in Davis et al. for saving the cost of memory.
- 8. In re claim 9, Dye shows a graphics controllers chip [212, fig 5].
- 9. In re claim 10, Dye shows a graphics engine [212, fig 5].
- 10. Claims 11-13, 16, 17, 20, 21, 25-26 are rejected under the same rationale as discussed above in claims 1-3, 6, 9, and 10.
- 11. Claims 4, 5, 7, 8, 14, 15, 18, 19, 22-24, 27, and 28 rejected under 35 U.S.C. 103(a) as being unpatentable over Dye, US Patent no. 6,173,381, in view of Davis

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et al., US Patent no. 4,991,169, as applied to claims 1, 2, 9, 10-12, 16-17, 20-21 and 25-26 above.

- 12. In re claims 4, 5, 7 and 8, Dye does not explicitly show a circular FIFO buffer, a double buffer, a triple buffer, a checking mechanism. Official Notice is taken that both the concept and the advantages of providing for a circular FIFO buffer, a double buffer, a triple buffer, a checking mechanism are old and well known in the art. Therefore, it would have been obvious to the ordinary skilled person in the art at the time the invention was made to include the FIFO, circular FIFO buffer, double buffer, triple buffer, checking mechanism in Dye for more flexible device by allowing it to operate in multiple configurations and more reliable system by controlling and predicting data flow.
- 13. Claims 14-15, 18-19, 22-24, 27, and 28 are rejected under the same rationale as discussed above in claims 4, 5, 7 and 8.

Response to Arguments

Applicant's amendment with arguments, filed on 6/1/2002, has been fully considered but they are not persuasive.

In the remarks, applicants argued in substance that the amended-claimed invention does not show "the engine ... manages the storage element via the interface".

The rejection states as above that "a storage element" is 230 of figure 5 instead of 204 of figure 5 as stated in the previous office action. Therefore, Dye shows the limitation: the engine ... manages the storage element [230, fig 5] via the interface [202, fig 5].

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Conclusion

Applicant's arguments with respect to claims 1-28 have been considered but they are not persuasive. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a).

Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any response to this action should be mailed to:

Mail Stop ____ Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

The centralized fax number is 703 872-9306.

The centralized hand carry paper drop off location is:

U.S. Patent and Trademark Office
2011 South Clark Place
Customer Window
Crystal Plaza Two, Lobby, Room 1B03

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Any inquiry of a general nature or relating to the status of this application should be directed to the technology center receptionist whose telephone number is (703) 306-5631.

Direct any inquiries concerning drawing review to the Drawing Review Branch (703) 305-8404.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Harold Kim whose telephone numbers are 703-305-1948 (until 10/12/2004), and 571-272-4148 (after 10/12/2004). The examiner can normally be reached on Monday-Thursday 6AM-4PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey Gaffin can be reached on 703-308-3301. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Patent Examiner

September 23, 2004/HK

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